

TEST CASE DOCUMENTATION AND TESTING RESULTS

LSTC-QA-LS-DYNA-AWG-CI-2-3

TEST CASE ID AWG-CI-2

Joints_Fasteners Shear Loading

Tested with LS-DYNA® R10.2 Revision 134743

Tuesday 12th March, 2019

Warranty Disclaimer:

The test case(s) described herein are for illustrative purposes only. LSTC does not warrant that a user of these or other LS-DYNA features will experience the same or similar results or that a feature will meet the user's particular requirements or operate error free. FURTHERMORE, THERE ARE NO WARRANTIES, EITHER EXPRESS OR IMPLIED, ORAL OR WRITTEN, WITH RESPECT TO THE DOCUMENTATION AND SOFTWARE DESCRIBED HEREIN INCLUDING, BUT NOT LIMITED TO ANY IMPLIED WARRANTIES (i) OF MERCHANTABILITY, OR (ii) FITNESS FOR A PARTICULAR PURPOSES, OR (iii) ARISING FROM COURSE OF PERFORMANCE OR DEALING, OR FROM USAGE OF TRADE OR. THE REMEDIES SET FORTH HEREIN ARE EXCLUSIVE AND IN LIEU OF ALL OTHER REMEDIES FOR BREACH OF WARRANTY.

Document Information	
Confidentiality	external use
Document Identifier	LSTC-QA-LS-DYNA-AWG-CI-2-3
Author(s)	Prepared by LS-DYNA® Aerospace Working Group
Number of pages	18
Date created	Tuesday 12 th March, 2019
Distribution	LS-DYNA® Aerospace Working Group / internal LSTC QA

Contents

1	Introduction	1
1.1	Purpose of this Document	1
2	Test Case Information	2
3	Test Case Specification	3
3.1	Test Case Purpose	3
3.2	Test Case Description	4
3.3	Model Description	5
4	Test Specifications	7
4.1	Test Case Targets	7
4.2	Pass/Fail Criteria	8
5	Test Case Results	9
5.1	Software and Hardware Specifications	9
5.2	Results Summary	10
5.3	Result Details	11
5.3.1	Sub Test Case ID 1-Test Target 1	12
5.3.2	Sub Test Case ID 1-Test Target 2	13
5.3.3	Sub Test Case ID 2-Test Target 3	14
5.3.4	Sub Test Case ID 2-Test Target 4	15
5.3.5	Sub Test Case ID 1-CPU time	16
5.3.6	Sub Test Case ID 2-CPU time	17
	References	18

1 Introduction

1.1 Purpose of this Document

This document specifies the test case AWG-CI-2. It provides general test case information like name and ID as well as information to the confidentiality, status, and classification of the test case.

A detailed description of the test case is given, the purpose of the test case is defined, and the tested features are named. The test case specifications also state the target measures for testing and the expected results, as well as their pass and fail criteria.

Testing results are provided in section 5 for the therein mentioned LS-DYNA® version and platforms.

2 Test Case Information

Test Case Summary	
Confidentiality	external use
Test Case Name	Joints_Fasteners Shear Loading
Test Case ID	AWG-CI-2
Test Case Status	active
Test Case Classification	Example
Test Case Source	The Boeing Company
Tested Keyword	*ELEMENT_SOLID, *ELEMENT_BEAM
Member of Test Suite	AWG CI SUITE
Metadata	AWG CI

Table 1: Test Case Summary

3 Test Case Specification

3.1 Test Case Purpose

The purpose of Test Case ID AWG-CI-2 is the comparison of results from different cpu architectures for the single shear joint models using 3D solid element or 1D beam element. The reliability and consistency of LS-DYNA® as a finite element solver for this test cases is evaluated by performing analyses on different cpu architecture platforms.

3.2 Test Case Description

This test case is a comparison between a 3D solid element and 1D beam element representation of a single shear joint subjected to shear loading and preload.

3.3 Model Description

Two aluminum (2024-T351) plates (5.0 in x 0.75 in x 0.75 in) are connected together through steel alloy (4340) fasteners. Fasteners assembly includes bolt head, bolt shank, washers, and nut. Bolt diameter is 0.25 in. The cross section side view shows two plates attached with fasteners defined as 3D solid elements (Sub Test Case 1) or as 1D beam elements (Sub Test Case 2), please see figure 1. Contact between the bolts and the plates is defined using *CONTACT_AUTOMATIC_SINGLE_SURFACE for both the beam and solid models.

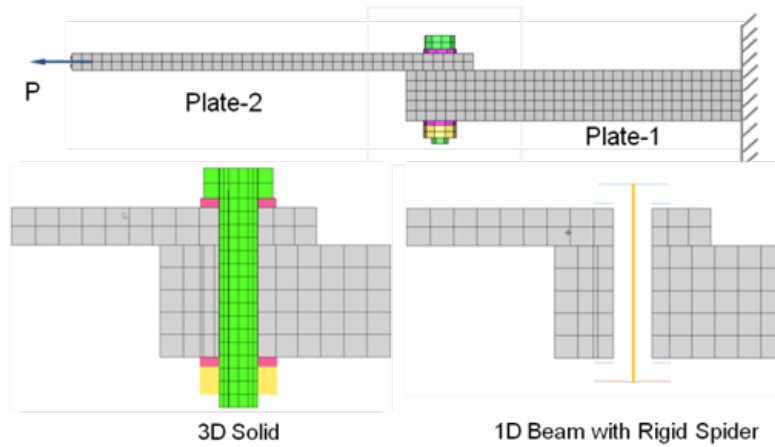


Figure 1: Joint_Fasteners Shear Loading Model

The model specifications can be found in table 2. The material definitions and their parameters can be found in the input deck. The input deck names of the two sub test cases are listed in table 3.

FEA Model information		
Sub Test Case ID ¹	1	2
Nodes	2522	2146
Solid elements	1579	1267
Shell elements	0	78
Beam elements	0	83
Material type list	MAT_1, MAT_24	MAT_1, MAT_24, MAT_5
Parts	6	8
Units	in (length), s (time), lbf-s ² /in (mass), lbf (force)	

¹ Sub Test Case ID refers to the ID's in table 3

Table 2: FEA Model Information

Sub Test ID	Fastener Element Type	Input Deck Name
1	Solid element	bolt_solid.key
2	beam element	bolt_spider.key

Table 3: Specification of sub test cases

4 Test Specifications

4.1 Test Case Targets

Table 4 displays the test case targets. The test case targets specify values or a series of values taken from the finite element analysis solution of the test case. The test case targets of the two sub test cases are the resultant displacement of a node at the tip of the upper plate (Plate-2 in figure 1) representing the maximum resultant displacement, and the Von Mises stress of an element around the bolt hole at the bottom face of the upper plate representing the maximum Von Mises stress on the upper plate.

Test Case Targets					
Target number	Sub Test Case ID	output	component type	components id	retrieved from
1	1	nodout	Resultant Displacement	71874	binout/nodout file
2	1	elout	Von Mises Stress	65559	binout/elout file
3	2	nodout	Resultant Displacement	75022	binout/nodout file
4	2	elout	Von Mises Stress	66872	binout/elout file

Table 4: Test Case targets for Test Case ID AWG-CI-2

4.2 Pass/Fail Criteria

These are the Pass/Fail criteria used for the Test Case ID AWG-CI-2.

The test case passes if the test case target data falls within the corridor bounds. Otherwise the test fails.

The test case corridors are upper and lower bounds for the test case targets. They were defined based on the test target data obtained with LS-DYNA® R9 Revision 118235 binaries by the following process:

- For a specific test case target, interpolate the data from different platform and executable (R9 Revision 118235) combinations, so that the time domain is the same.
- Calculate the upper and lower bounds by:

$$bound_{up}(i) = max(i) + 0.2 \times [max(i) - min(i)] + 0.05 \times peak$$

$$bound_{low}(i) = min(i) - 0.2 \times [max(i) - min(i)] - 0.05 \times peak$$

where $max(i)$, $min(i)$ are the maximum and minimum values at the i_{th} time step across all platforms and executable (R9 Revision 118235) combinations the test case was calculated with, $peak$ is the maximum absolute y value across the whole time domain, $bound_{up}(i)$ and $bound_{low}(i)$ are the upper and lower bounds for the i_{th} time step.

5 Test Case Results

5.1 Software and Hardware Specifications

In order to ensure cross-platform consistency, the herein mentioned sub test cases are run on platforms specified in table 5 and the results are calculated with software versions defined in table 6.

Platform Name	Operating system	CPU type	MPI-Protocol	Number of cpu's ¹
mars	CentOS 6.5	Intel® Xeon® E5- 2640 @ 2.50GHz	Platform MPI 8.2.0.0	4
dinar3b	SUSE LES 11	AMD® Opteron ® 6276 @ 2300MHz	Platform MPI 8.2.0.0	4

¹ Number of cpu's used for calculation of the test case

Table 5: Used Platforms and CPU Type's

Product	Version	Release	Revision	Parallel type ¹	Precision ²	executable
LS-DYNA®	971	R10.2	134743	SMP	SP	ls971.134743.R10.2
LS-DYNA®	971	R10.2	134743	SMP	DP	ld971.134743.R10.2
LS-DYNA®	971	R10.2	134743	MPP	SP	mpp971.134743.R10.2
LS-DYNA®	971	R10.2	134743	MPP	DP	mpd971.134743.R10.2

¹ MPP = Massively Parallel Processing, SMP = Symmetric Multiprocessing

² SP = single precision, DP = double precision

Table 6: Tested LS-DYNA® version

5.2 Results Summary

Table 7 contains the results of the Test Case ID AWG-CI-2 completed with all combinations of software and hardware defined in section 5.1 (4 * 2 total cases).

Details on the test results can be found in the section 5.3.

The table 7 validation summary is:

- **PASS** - Pass criteria in section 4.2 is attained.
- **FAILED** - Pass criteria in section 4.2 is not attained.
- **ERROR** - sub test case terminates due to error.
- **N/A** - sub test case was not calculated.

Test Case ID	PASS/FAILED
2-1	PASS
2-2	PASS

Table 7: Results summary for Test Case ID AWG-CI-2

5.3 Result Details

The following subsections contain detailed results for the Test Case ID AWG-CI-2 for LS-DYNA® R10.2 Revision 134743.

For each sub test case defined in section 3.3 there is a graph displaying the time history of the result target defined in section 4.1 for the platform and software version combinations defined in section 5.1.

The title of the graph states the test case ID and the name of input deck. The legend contains the result file name, output, platform, executable and number of cpu's separated by comma. A minus sign before the number of cpu's refers to the compatibility option for SMP calculations (see [1] for details on this option).

Example for title and legend:

Title:

'AWG_CI_TEST_CASE.2: bolt_solid...key' states the test case ID 2 and name of the input deck.

Legend:

'nodout_rdisp71874,mars,ls971.134743.R10.2,4' states that the graph shows the resultant-displacement of node 71874 derived from the 'nodout' output file for an input deck which was calculated on the 'mars' platform with a LS-DYNA® R10.2 Revision 134743 binary (SMP, single precision) on four processor.

5.3.1 Sub Test Case ID 1-Test Target 1

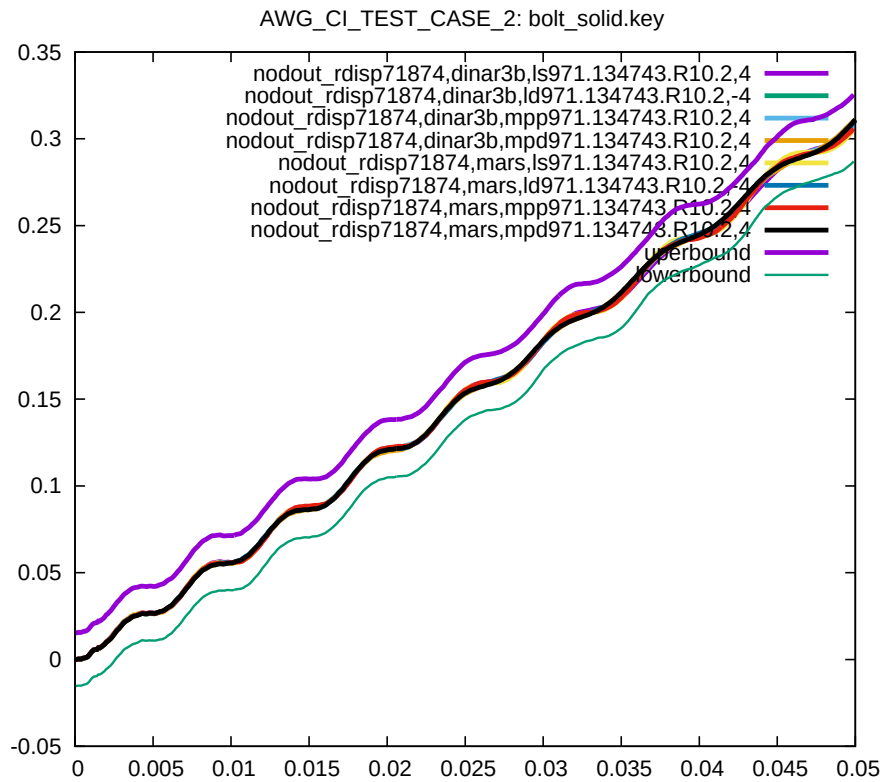


Figure 2: Cross platform results, nodal resultant displacement node 71874, sub test case ID 1

5.3.2 Sub Test Case ID 1-Test Target 2

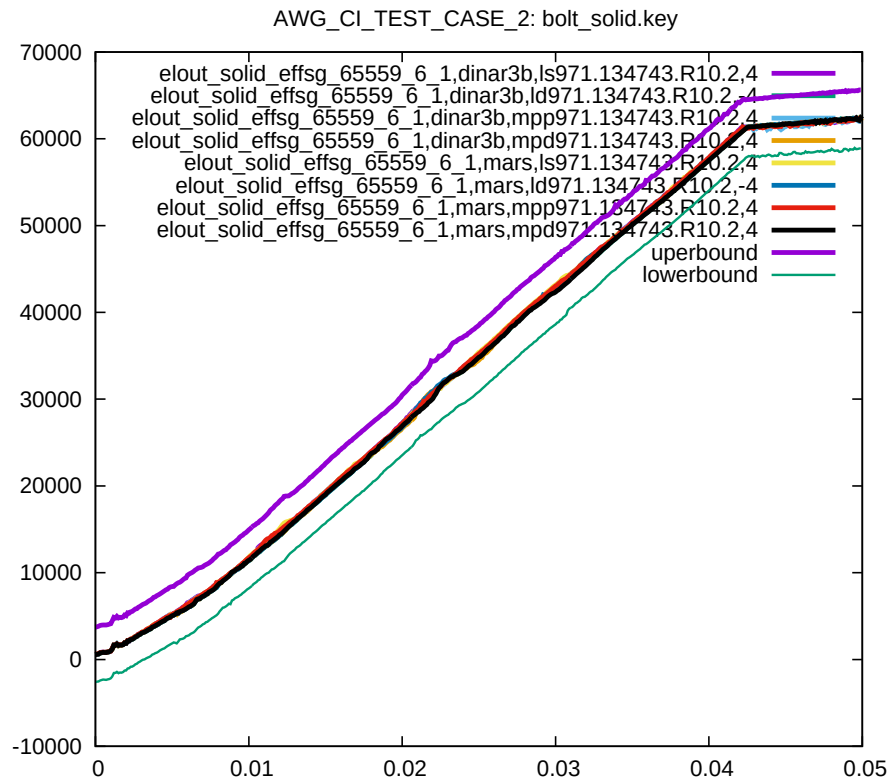


Figure 3: Cross platform results, Von Mises stress of element 65559, sub test case ID 1

5.3.3 Sub Test Case ID 2-Test Target 3

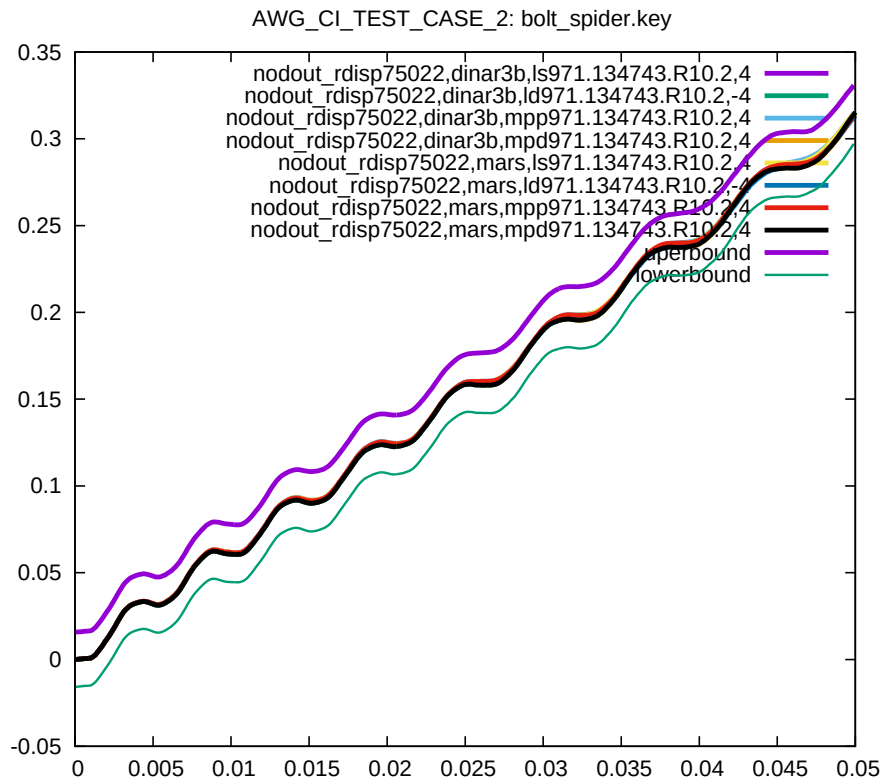


Figure 4: Cross platform results, nodal resultant displacement node 75022, sub test case ID 2

5.3.4 Sub Test Case ID 2-Test Target 4

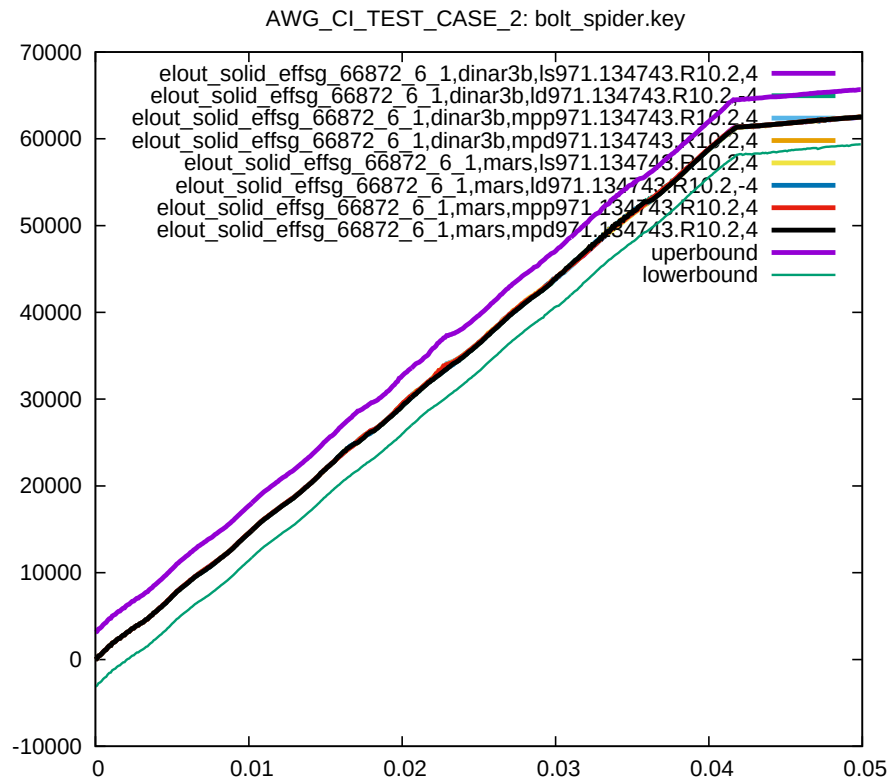


Figure 5: Cross platform results, Von Mises stress of element 66872, sub test case ID 2

5.3.5 Sub Test Case ID 1-CPU time

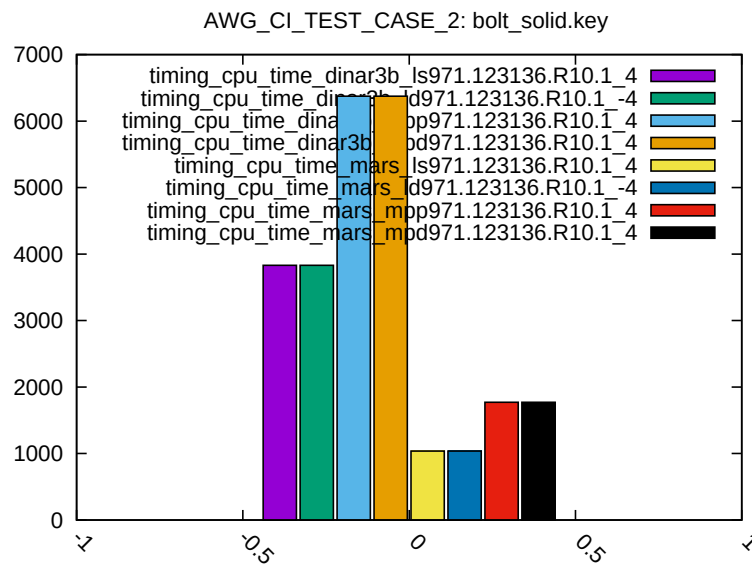


Figure 6: Cross platform results, CPU time(s)

5.3.6 Sub Test Case ID 2-CPU time

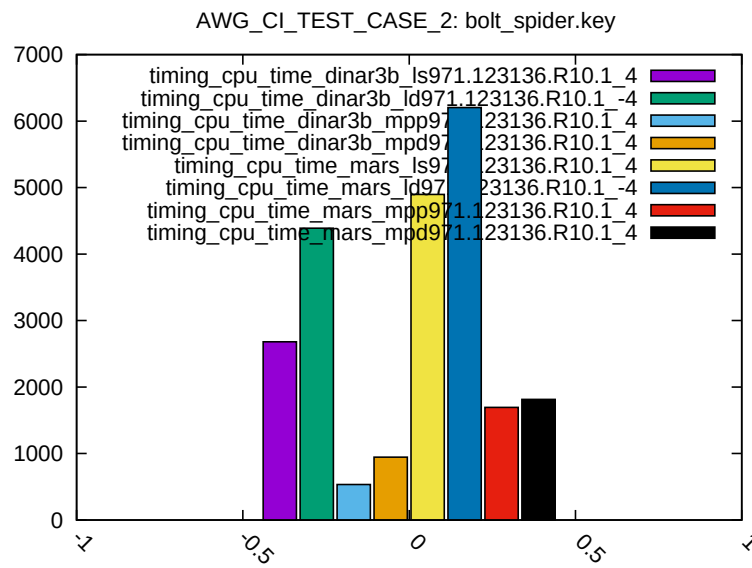


Figure 7: Cross platform results, CPU time(s)

References

- [1] LSTC, *LS-DYNA KEYWORD USER MANUAL*, 7374 Las Positas Road, Livermore, CA, 94551, USA, version 971 ed., May 2007.