

TEST CASE DOCUMENTATION AND TESTING RESULTS

LSTC-QA-LS-DYNA-AWG-ERIF-11-6

TEST CASE ID AWG-ERIF-11

***CONSTRAINED Keyword Test Case**

Tested with LS-DYNA® R8.0.0 Revision 96167

Tuesday 10th March, 2015

Warranty Disclaimer:

The test case(s) described herein are for illustrative purposes only. LSTC does not warrant that a user of these or other LS-DYNA features will experience the same or similar results or that a feature will meet the user's particular requirements or operate error free. FURTHERMORE, THERE ARE NO WARRANTIES, EITHER EXPRESS OR IMPLIED, ORAL OR WRITTEN, WITH RESPECT TO THE DOCUMENTATION AND SOFTWARE DESCRIBED HEREIN INCLUDING, BUT NOT LIMITED TO ANY IMPLIED WARRANTIES (i) OF MERCHANTABILITY, OR (ii) FITNESS FOR A PARTICULAR PURPOSES, OR (iii) ARISING FROM COURSE OF PERFORMANCE OR DEALING, OR FROM USAGE OF TRADE OR. THE REMEDIES SET FORTH HEREIN ARE EXCLUSIVE AND IN LIEU OF ALL OTHER REMEDIES FOR BREACH OF WARRANTY.

Document Information	
Confidentiality	external use
Document Identifier	LSTC-QA-LS-DYNA-AWG-ERIF-11-6
Author(s)	Prepared by LS-DYNA® Aerospace Working Group
Number of pages	23
Date created	Tuesday 10 th March, 2015
Distribution	LS-DYNA® Aerospace Working Group / internal LSTC QA

Contents

1	Introduction	1
1.1	Purpose of this Document	1
2	Test Case Information	2
3	Test Case Specification	3
3.1	Test Case Purpose	3
3.2	Test Case Description	4
3.3	Model Description	5
4	Test Specifications	7
4.1	Test Case Targets	7
4.2	Pass/Fail Criteria	7
5	Test Case Results	8
5.1	Software and Hardware Specifications	8
5.2	Results Summary	9
5.3	Result Details	10
5.3.1	Sub Test Case ID 1 - Test Target 1	11
5.3.2	Sub Test Case ID 1 - Test Target 2	11
5.3.3	Sub Test Case ID 1 - Test Target 3	12
5.3.4	Sub Test Case ID 1 - Test Target 4	12
5.3.5	Sub Test Case ID 1 - Test Target 5	13
5.3.6	Sub Test Case ID 1 - Test Target 6	13
5.3.7	Sub Test Case ID 1 - Test Target 7	14
5.3.8	Sub Test Case ID 1 - Test Target 8	14
5.3.9	Sub Test Case ID 1 - Test Target 9	15
5.3.10	Sub Test Case ID 1 - Test Target 10	15
5.3.11	Sub Test Case ID 1 - Test Target 11	16
5.3.12	Sub Test Case ID 1 - Test Target 12	16
5.3.13	Sub Test Case ID 2 - Test Target 1	17
5.3.14	Sub Test Case ID 2 - Test Target 2	17
5.3.15	Sub Test Case ID 2 - Test Target 3	18
5.3.16	Sub Test Case ID 2 - Test Target 4	18
5.3.17	Sub Test Case ID 2 - Test Target 5	19
5.3.18	Sub Test Case ID 2 - Test Target 6	19
5.3.19	Sub Test Case ID 2 - Test Target 7	20
5.3.20	Sub Test Case ID 2 - Test Target 8	20
5.3.21	Sub Test Case ID 2 - Test Target 9	21
5.3.22	Sub Test Case ID 2 - Test Target 10	21
5.3.23	Sub Test Case ID 2 - Test Target 11	22
5.3.24	Sub Test Case ID 2 - Test Target 12	22
	References	23

1 Introduction

1.1 Purpose of this Document

This document specifies the test case AWG-ERIF-11. It provides general test case information like name and ID as well as information to the confidentiality, status, and classification of the test case.

A detailed description of the test case is given, the purpose of the test case is defined, and the tested features are named. The test case specifications also state the target measures for testing and the expected results, as well as their pass and fail criteria.

Testing results are provided in section 5 for the therein mentioned LS-DYNA® version and platforms.

2 Test Case Information

Test Case Summary	
Confidentiality	external use
Test Case Name	*CONSTRAINED Keyword Test Case
Test Case ID	AWG-ERIF-11
Test Case Status	active
Test Case Classification	Example
Test Case Source	NASA
Tested Keyword	*CONSTRAINED_INTERPOLATION, *CONSTRAINED_NODAL_RIGID_BODY
Member of Test Suite	AWG ERIF SUITE
Metadata	AWG ERIF

Table 1: Test Case Summary

3 Test Case Specification

3.1 Test Case Purpose

The purpose of Test Case ID AWG-ERIF-11 is the comparison of results from different cpu architectures for a basic model containing *CONSTRAINED.INTERPOLATION and *CONSTRAINED_NODAL_RIGID_BODY keyword.

The reliability and consistency of LS-DYNA® as a finite element solver for these test cases is evaluated by performing analyses on different cpu architecture platforms.

3.2 Test Case Description

This Test Case contains two approaches to model constraints commonly used in the aerospace industry.

The Test Case contains two separate cylinders (see figure 1), one with a center node connected with a nodal rigid body constraint and the other with an interpolation constraint. In the nodal rigid body, no degrees of freedom are released. In the interpolation model, the circumferential nodes are define as independent nodes.

The cylinder radius and length for both cylinders is 10 inches and the base nodes are held fixed.

The thickness of the cylinders is 0.1 inches and the material is a linear elastic aluminum.

The loads are simultaneously applied to both cylinders and they consist of axial forces and bending moments.

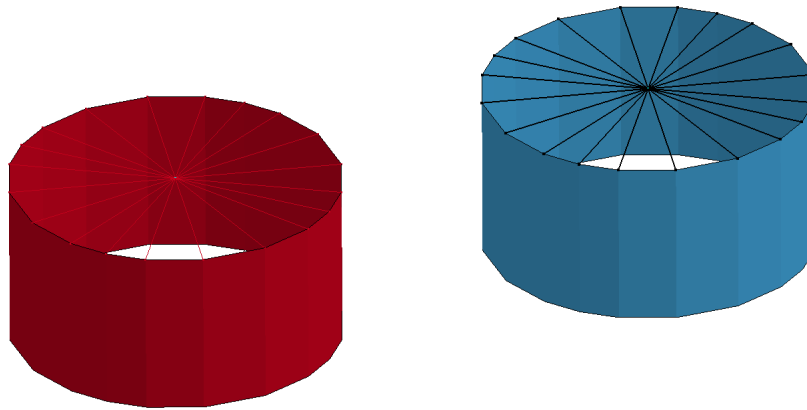


Figure 1: Model sketch: Two cylinders with constraints

Table 2 contains a short summary of the physical model set up.

Physical Model Information	
cylinder radius	10 inches
cylinder length	10 inches
cylinder thickness	0.1 inches
material	linear elastic aluminum

Table 2: Model set-up data

3.3 Model Description

The cylinders are discretized with shell elements (see figure 2).

Forces and moments are applied at the center nodes.

The model specifications can be found in table 3, and table 4 defines the sub test case specification.

The material definitions and their parameters can be found in the input decks.

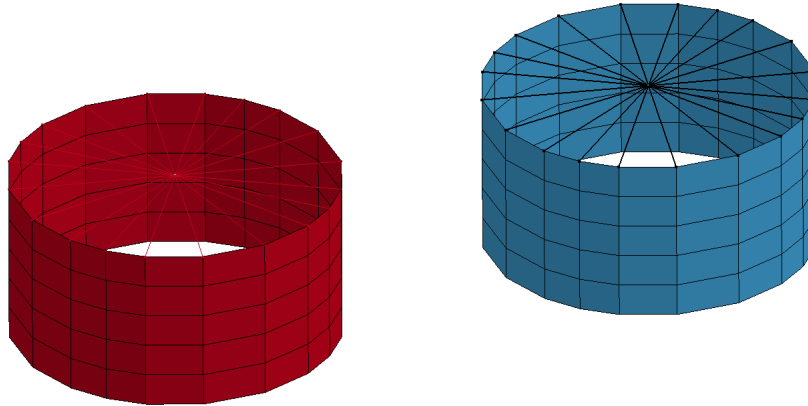


Figure 2: FEA model: Cylinders with constraints

FEA Model information		
Sub Test Case ID ¹	1	2
Nodes	242	242
Shell elements	200	200
Shell materials	2	2
Mass elements	2	2
Nodal rigid bodies	1	1
Units	<i>in</i> (length), <i>sec</i> (time), <i>lbf · sec²/in</i> (mass), <i>lbf/in²</i> (stress), <i>lbf · in</i> (energy)	

¹ Sub Test Case ID refers to the ID's in table 4

Table 3: FEA Model Information

Sub test case ID 1 is a model which is calculated with an explicit time integration method and sub test case ID 2 is the same model setup calculated with an implicit solution method.

Sub Test ID	Input Deck Name
1	CONSTRAINED.TEST_CASE.EXPLICIT.k
2	CONSTRAINED.TEST_CASE.IMPLICIT.k

Table 4: Specification of sub test cases

4 Test Specifications

4.1 Test Case Targets

Table 5 displays the test case targets. The test case targets specify values or a series of values taken from the finite element analysis solution of the test case and they are used in a comparison of analysis results on different cpu architectures. They are chosen in a way that they are representative of the numerical model.

Test Case Targets				
Target number	output	component type	component id	retrieved from
1	nodal	x displacement	1000	binout/nodout file
2	nodal	y displacement	1000	binout/nodout file
3	nodal	z displacement	1000	binout/nodout file
4	nodal	x rotation	1000	binout/nodout file
5	nodal	y rotation	1000	binout/nodout file
6	nodal	z rotation	1000	binout/nodout file
7	nodal	x displacement	2000	binout/nodout file
8	nodal	y displacement	2000	binout/nodout file
9	nodal	z displacement	2000	binout/nodout file
10	nodal	x rotation	2000	binout/nodout file
11	nodal	y rotation	2000	binout/nodout file
12	nodal	z rotation	2000	binout/nodout file

Table 5: Test Case targets for Test Case ID AWG-ERIF-11

Test case targets are used to evaluate the cross cpu architecture consistency (see section 4.2).

4.2 Pass/Fail Criteria

These are the Pass/Fail criteria used for the cross cpu architecture consistency test of the Test Case ID AWG-ERIF-11.

The sub test case passes if all of the following criteria are reached:

- For a specific test case target, the maximum distance between an x-y pair of a slope of one cpu architecture/software version combination to at least one x-y pair of all other tested cpu architecture/software version combinations is 15% of the maximum slope value.

Otherwise the cross cpu architecture consistency test fails.

5 Test Case Results

5.1 Software and Hardware Specifications

In order to ensure cross-platform consistency, the herein mentioned sub test cases are run on platforms specified in table 6 and the results are calculated with software versions defined in table 7.

Platform Name	Operating system	CPU type	MPI-Protocol	Number of cpu's ¹
sandwich	SUSE LES 11.1	Intel® Xeon® E7- 8837 @ 2.67GHz	Platform MPI 8.2.0.0	4
ham	CentOS 5.4	AMD® Opteron® 8435 @ 800MHz	Platform MPI 8.1.0.0	4
sgi64d	SUSE LES 9.4 ²	Intel® Itanium® 2 @ 1.6GHz	SGI MPT 1.13	4

¹ Number of cpu's used for calculation of the test case

² SGI PROPAC 4

Table 6: Used Platforms and CPU Type's

Product	Version	Release	Revision	Parallel type ¹	Precision ²	executable
LS-DYNA®	971	R8.0.0	96167	SMP	SP	ls971.96167.R8.0.0
LS-DYNA®	971	R8.0.0	96167	SMP	DP	ld971.96167.R8.0.0
LS-DYNA®	971	R8.0.0	96167	MPP	SP	mpp971.96167.R8.0.0
LS-DYNA®	971	R8.0.0	96167	MPP	DP	mpd971.96167.R8.0.0

¹ MPP = Massively Parallel Processing, SMP = Symmetric Multiprocessing

² SP = single precision, DP = double precision

Table 7: Tested LS-DYNA® version

5.2 Results Summary

Table 8 contains the results of the Test Case ID AWG-ERIF-11 completed with all combinations of software and hardware defined in section 5.1 ($1 * 4 * 4 + 1 * 4 * 2$ total calculation runs). Details on the test results can be found in the section 5.3.

The table 8 cross cpu architecture consistency summary is:

- **PASS** - Pass criteria in section 4.2 is attained.
- **FAILED** - Pass criteria in section 4.2 is not attained.
- **ERROR** - sub test case terminates due to error.
- **N/A** - sub test case was not calculated.

Sub Test Case ID	PASS/FAILED
1	PASS
2	PASS

Table 8: Results summary for Test Case ID AWG-ERIF-11

5.3 Result Details

The following subsections contain detailed results for the Test Case ID AWG-ERIF-11 for LS-DYNA® R8.0.0 Revision 96167.

For each sub test case defined in section 3.3 there is a graph displaying the time history of the result target defined in section 4.1 for the platform and software version combinations defined in section 5.1.

The title of the graph states the name of the input deck, the result file name, and the output separated by underscores. The legend contains the result file name, output, platform, and executable. The last number of the legend specifies the number of cpu's used to calculate the example. A leading minus sign refers to the compatibility option for SMP calculations (see [1] for details on this option).

Example for title and legend:

Title:

'CONSTRAINED_TEST_CASE_EXPLICIT.k: nodout_xdisp1000' states that the input deck for sub test case 1 was used to calculate these results. The component displayed is the nodal displacement in x-direction for node 1000 derived from the 'nodout' output file.

Legend:

'nodout_xdisp1000_sandwich_ls971.96167.R8.0.0_4' states that the graph shows the nodal displacement in x-direction for node 1000 derived from the 'nodout' output file for an input deck which was calculated on the 'sandwich' platform with a LS-DYNA® R8.0.0 Revision 96167 binary (SMP, single precision) on four processors.

5.3.1 Sub Test Case ID 1 - Test Target 1

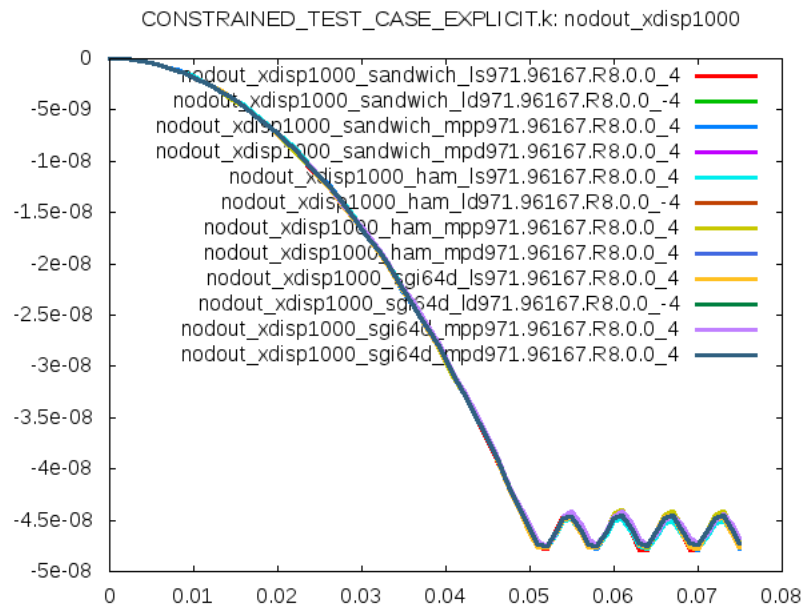


Figure 3: Cross platform results, nodal displacement in x-direction, node 1000, sub test case ID 1

5.3.2 Sub Test Case ID 1 - Test Target 2

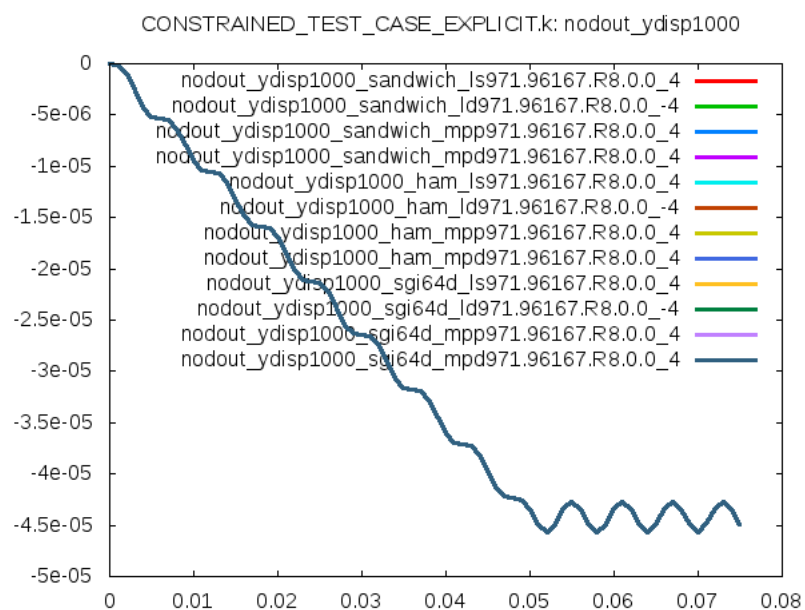


Figure 4: Cross platform results, nodal displacement in y-direction, node 1000, sub test case ID 1

5.3.3 Sub Test Case ID 1 - Test Target 3

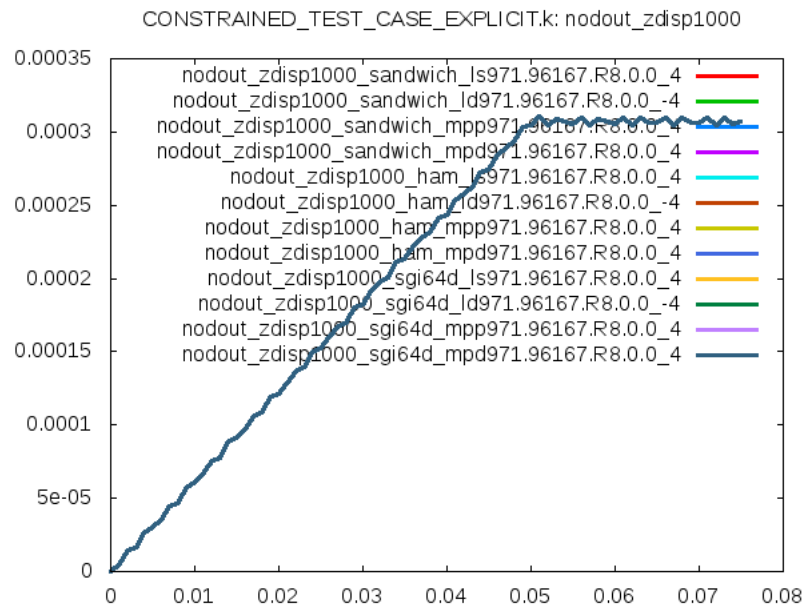


Figure 5: Cross platform results, nodal displacement in z-direction, node 1000, sub test case ID 1

5.3.4 Sub Test Case ID 1 - Test Target 4

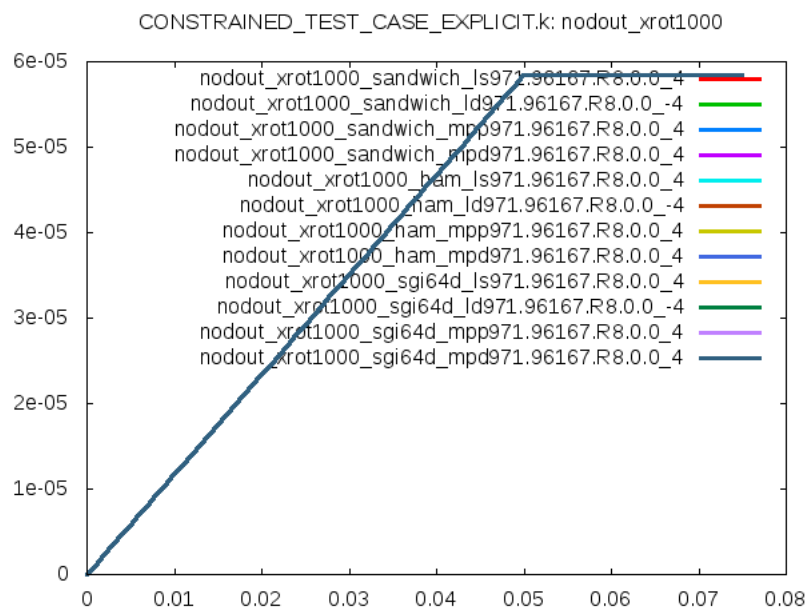


Figure 6: Cross platform results, nodal rotation around x-axis, node 1000, sub test case ID 1

5.3.5 Sub Test Case ID 1 - Test Target 5

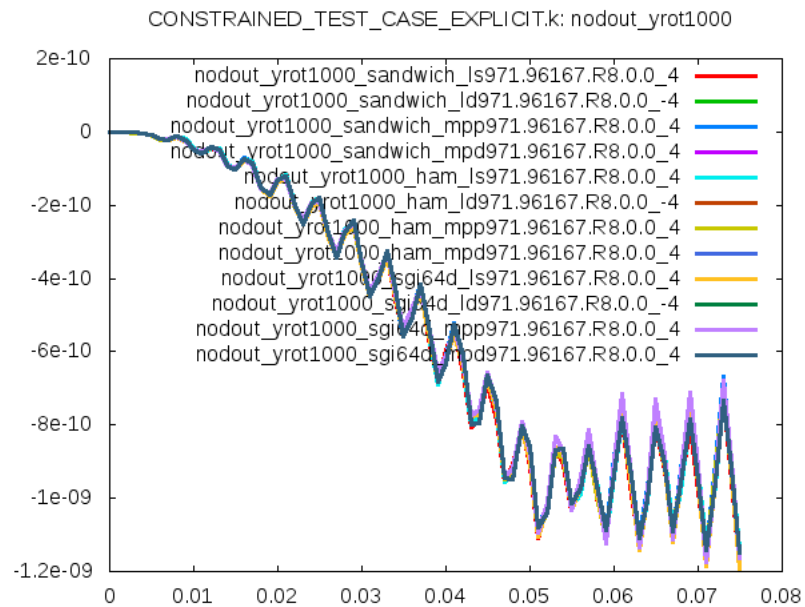


Figure 7: Cross platform results, nodal rotation around y-axis, node 1000, sub test case ID 1

5.3.6 Sub Test Case ID 1 - Test Target 6

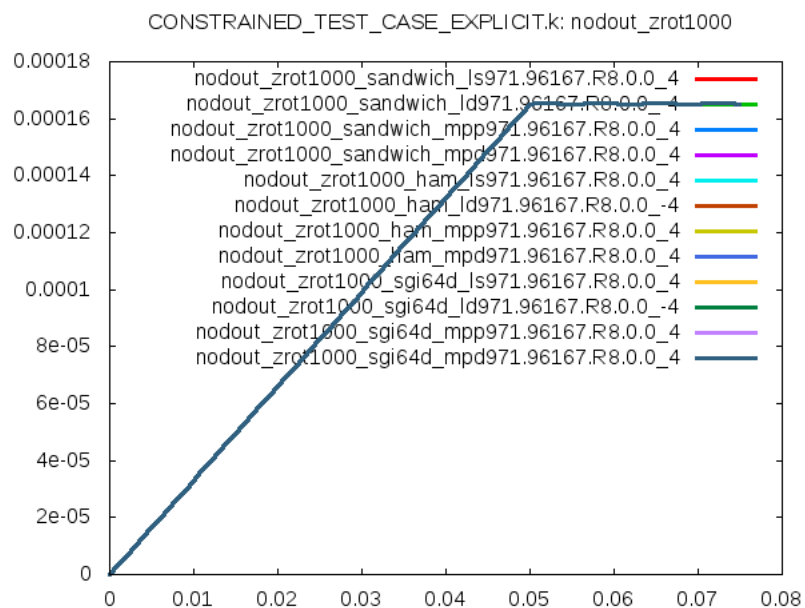


Figure 8: Cross platform results, nodal rotation around z-axis, node 1000, sub test case ID 1

5.3.7 Sub Test Case ID 1 - Test Target 7

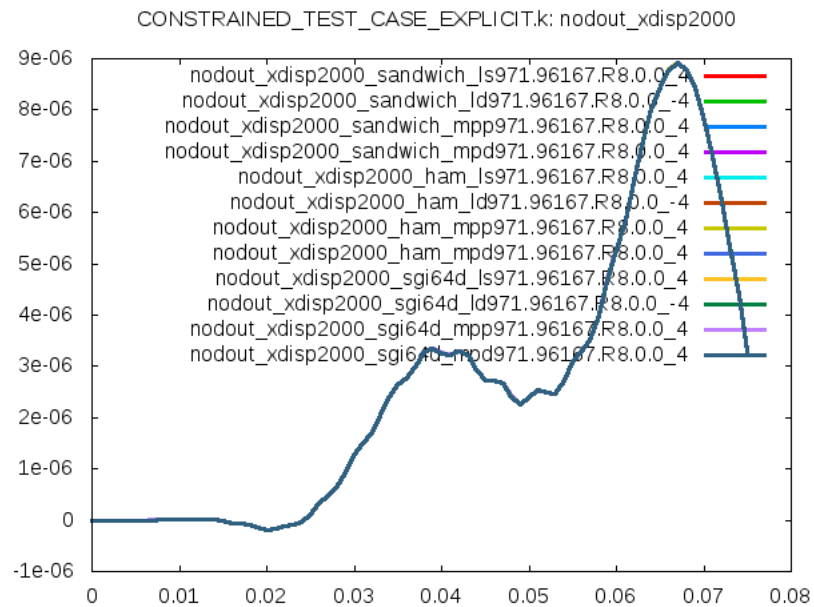


Figure 9: Cross platform results, nodal displacement in x-direction, node 2000, sub test case ID 1

5.3.8 Sub Test Case ID 1 - Test Target 8

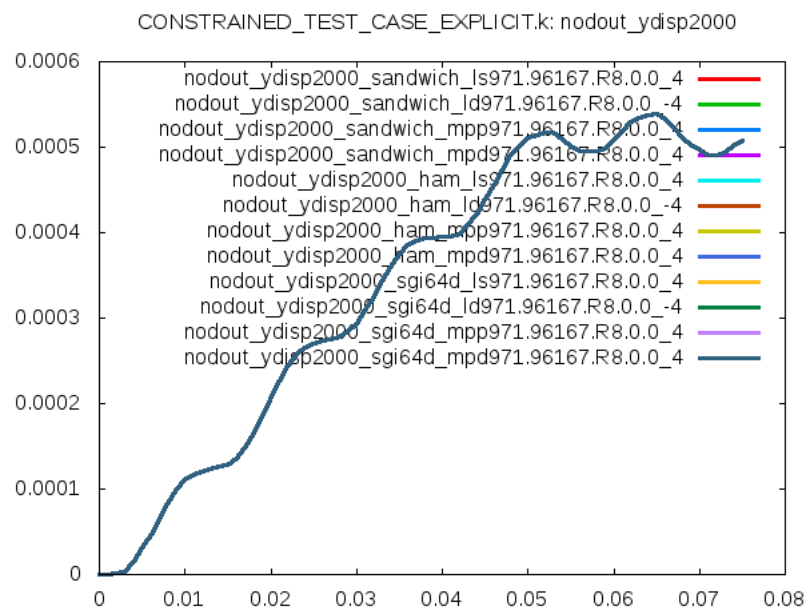


Figure 10: Cross platform results, nodal displacement in y-direction, node 2000, sub test case ID 1

5.3.9 Sub Test Case ID 1 - Test Target 9

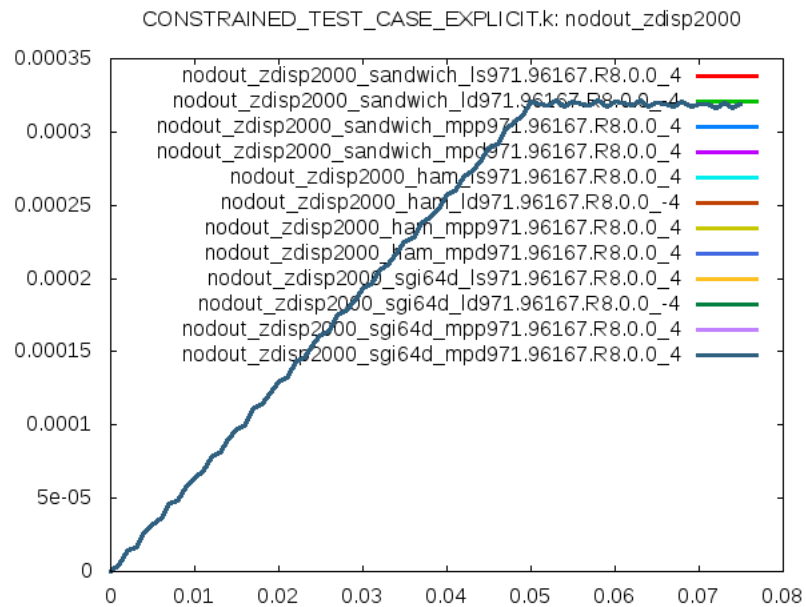


Figure 11: Cross platform results, nodal displacement in z-direction, node 2000, sub test case ID 1

5.3.10 Sub Test Case ID 1 - Test Target 10

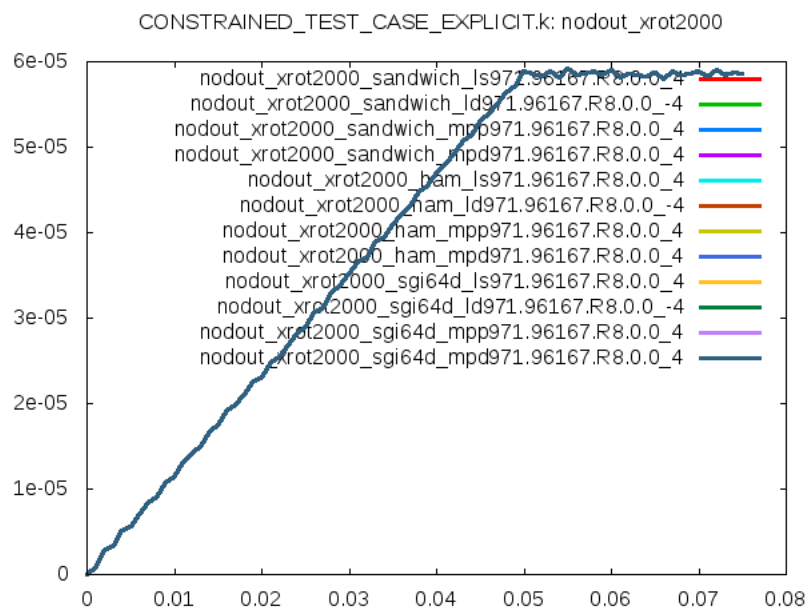


Figure 12: Cross platform results, nodal rotation around x-axis, node 2000, sub test case ID 1

5.3.11 Sub Test Case ID 1 - Test Target 11

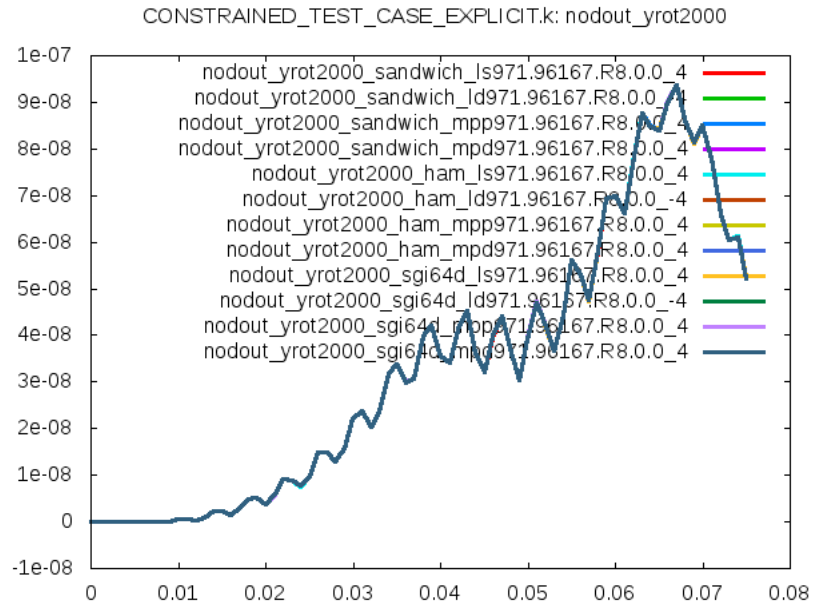


Figure 13: Cross platform results, nodal rotation around y-axis, node 2000, sub test case ID 1

5.3.12 Sub Test Case ID 1 - Test Target 12

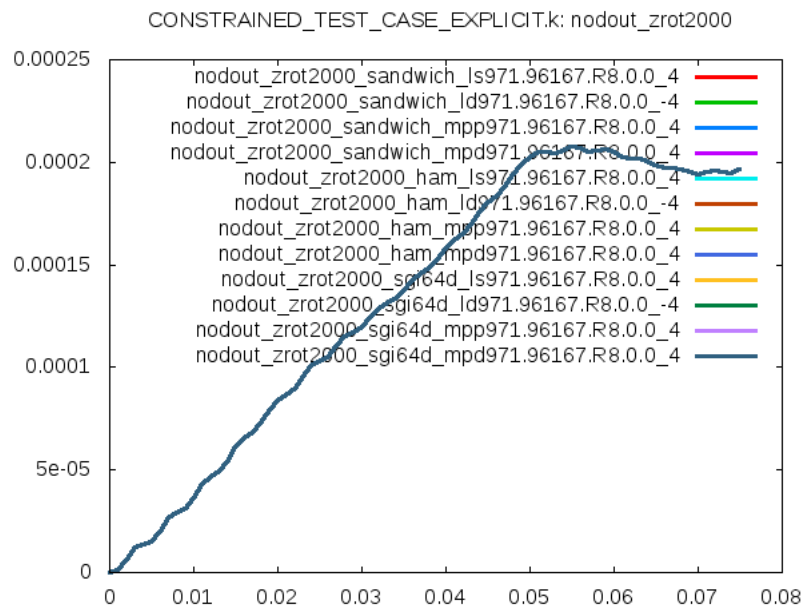


Figure 14: Cross platform results, nodal rotation around z-axis, node 2000, sub test case ID 1

5.3.13 Sub Test Case ID 2 - Test Target 1

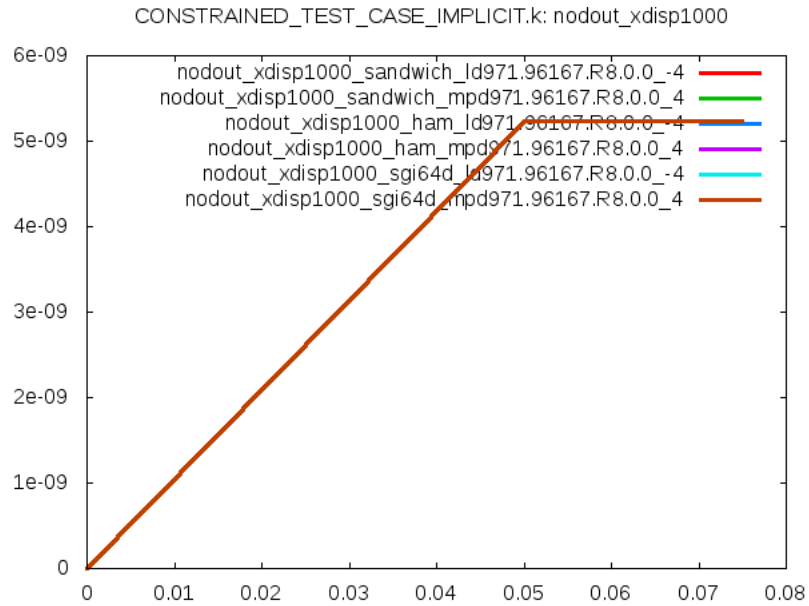


Figure 15: Cross platform results, nodal displacement in x-direction, node 1000, sub test case ID 2

5.3.14 Sub Test Case ID 2 - Test Target 2

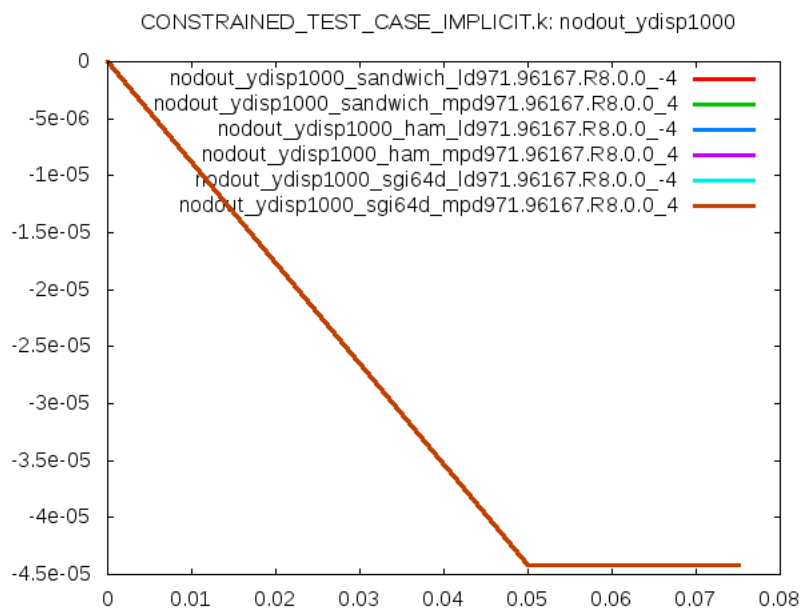


Figure 16: Cross platform results, nodal displacement in y-direction, node 1000, sub test case ID 2

5.3.15 Sub Test Case ID 2 - Test Target 3

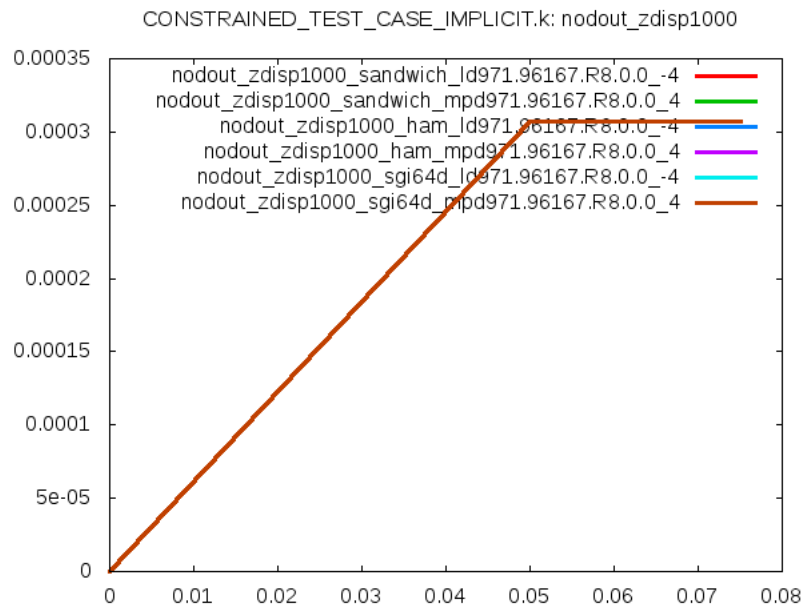


Figure 17: Cross platform results, nodal displacement in z-direction, node 1000, sub test case ID 2

5.3.16 Sub Test Case ID 2 - Test Target 4

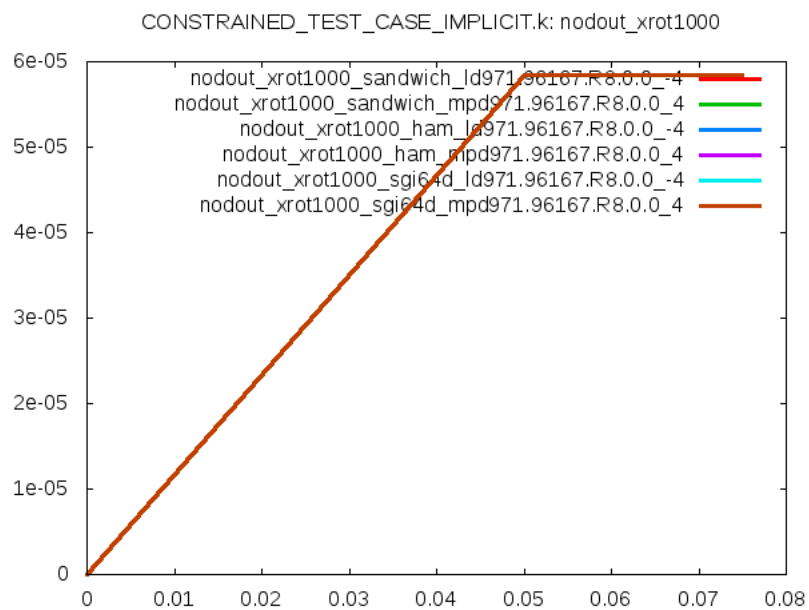


Figure 18: Cross platform results, nodal rotation around x-axis, node 1000, sub test case ID 2

5.3.17 Sub Test Case ID 2 - Test Target 5

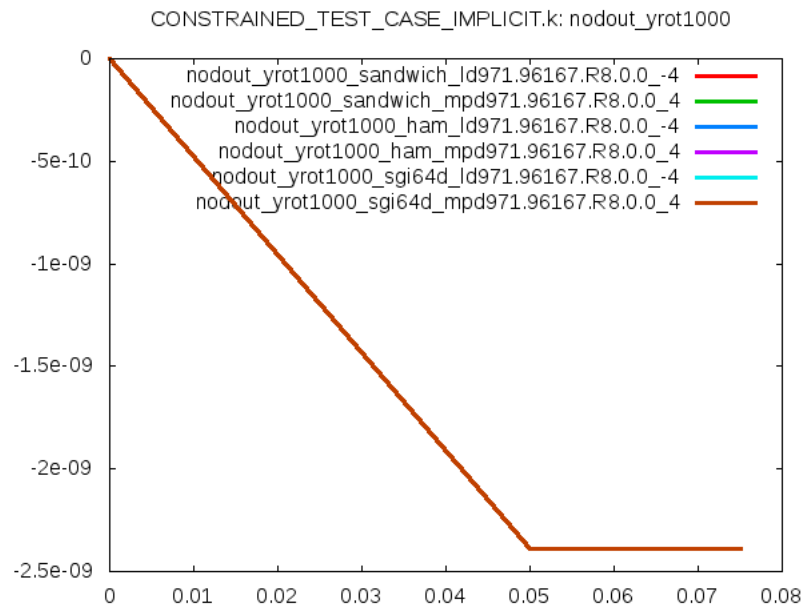


Figure 19: Cross platform results, nodal rotation around y-axis, node 1000, sub test case ID 2

5.3.18 Sub Test Case ID 2 - Test Target 6

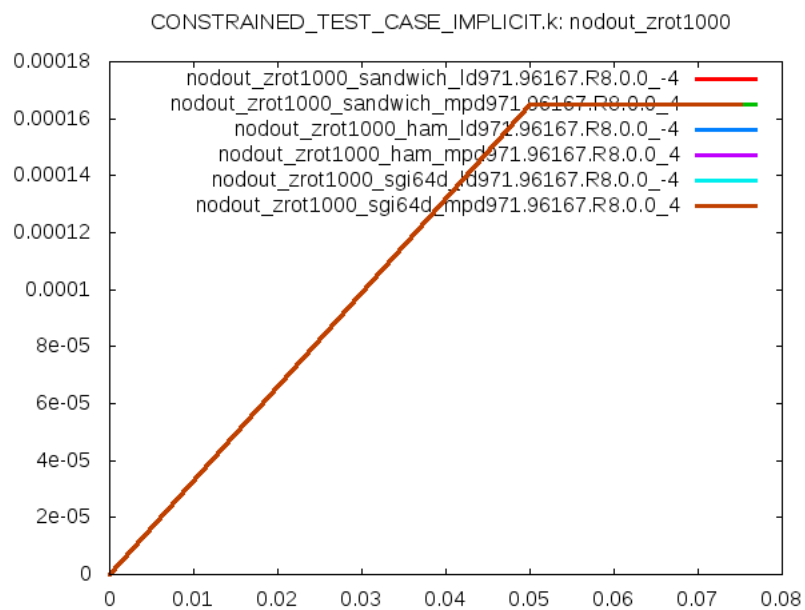


Figure 20: Cross platform results, nodal rotation around z-axis, node 1000, sub test case ID 2

5.3.19 Sub Test Case ID 2 - Test Target 7

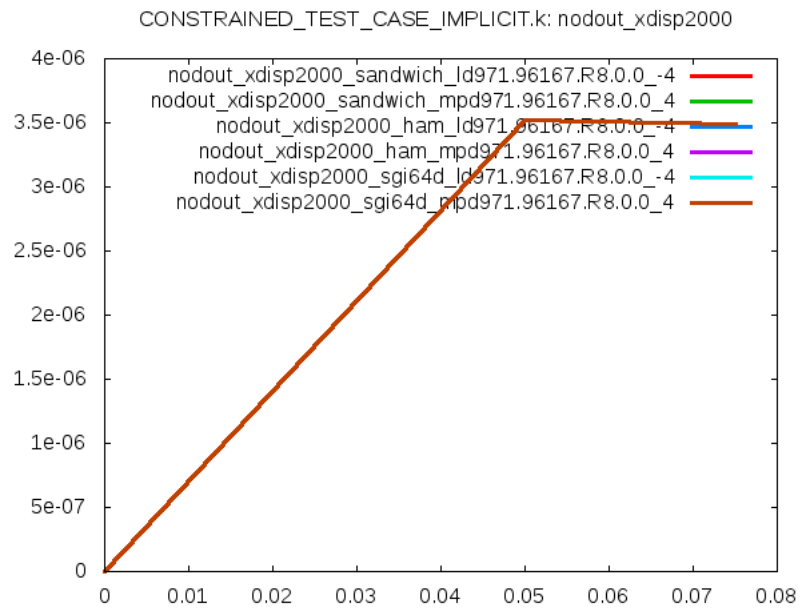


Figure 21: Cross platform results, nodal displacement in x-direction, node 2000, sub test case ID 2

5.3.20 Sub Test Case ID 2 - Test Target 8

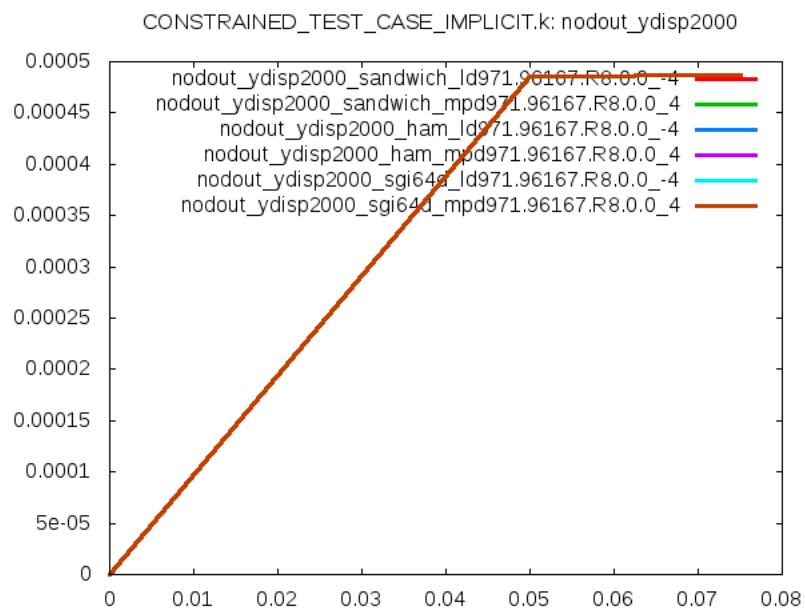


Figure 22: Cross platform results, nodal displacement in y-direction, node 2000, sub test case ID 2

5.3.21 Sub Test Case ID 2 - Test Target 9

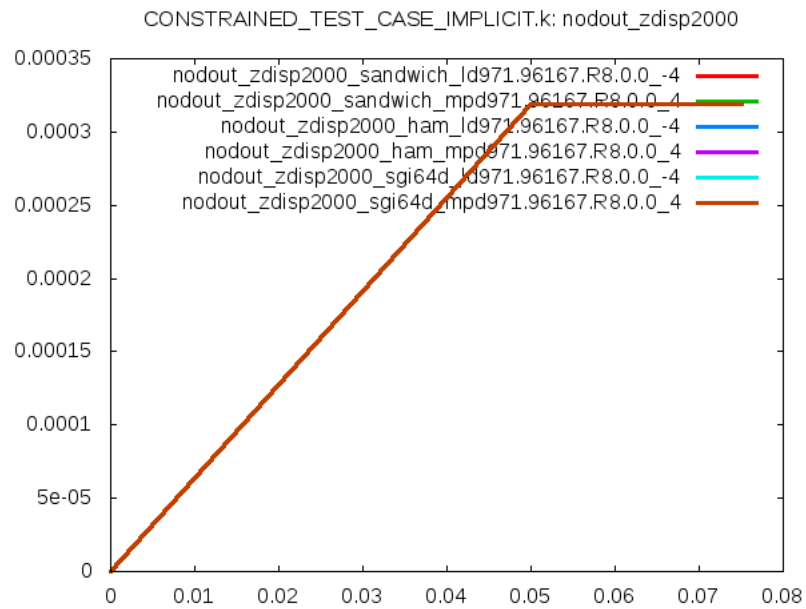


Figure 23: Cross platform results, nodal displacement in z-direction, node 2000, sub test case ID 2

5.3.22 Sub Test Case ID 2 - Test Target 10

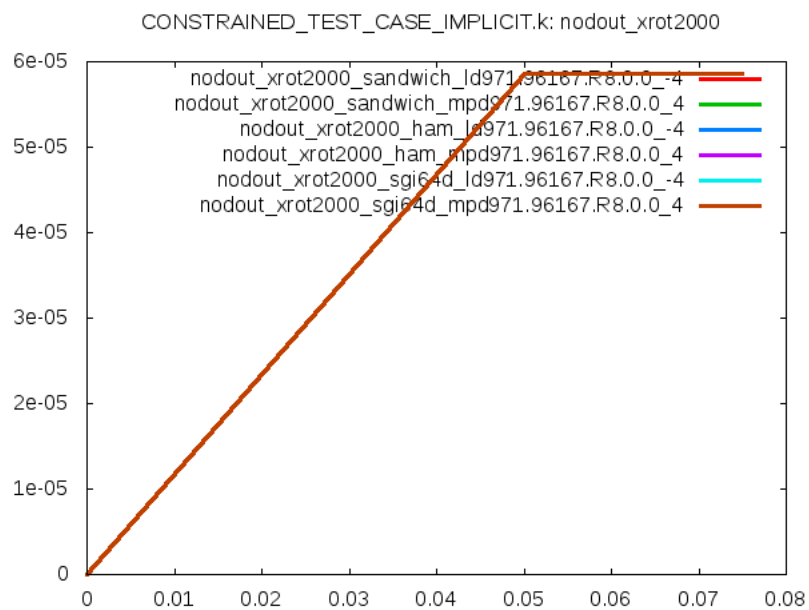


Figure 24: Cross platform results, nodal rotation around x-axis, node 2000, sub test case ID 2

5.3.23 Sub Test Case ID 2 - Test Target 11

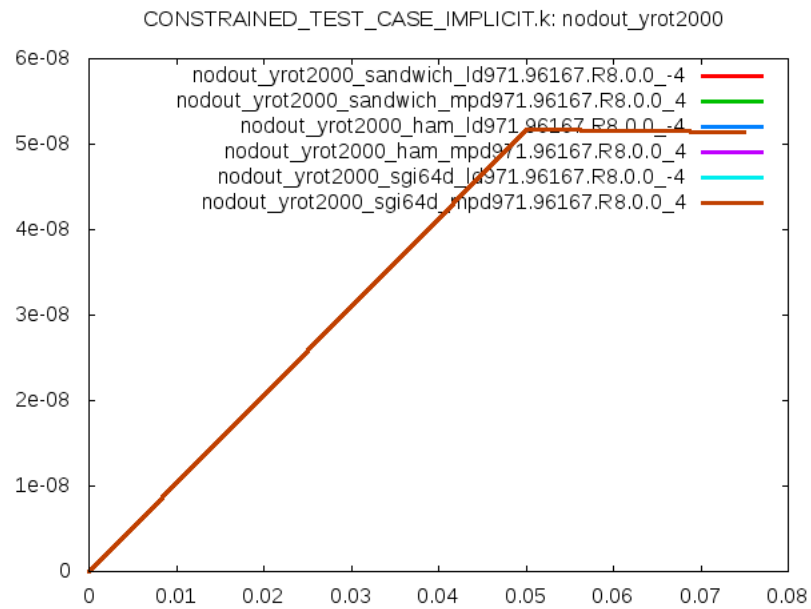


Figure 25: Cross platform results, nodal rotation around y-axis, node 2000, sub test case ID 2

5.3.24 Sub Test Case ID 2 - Test Target 12

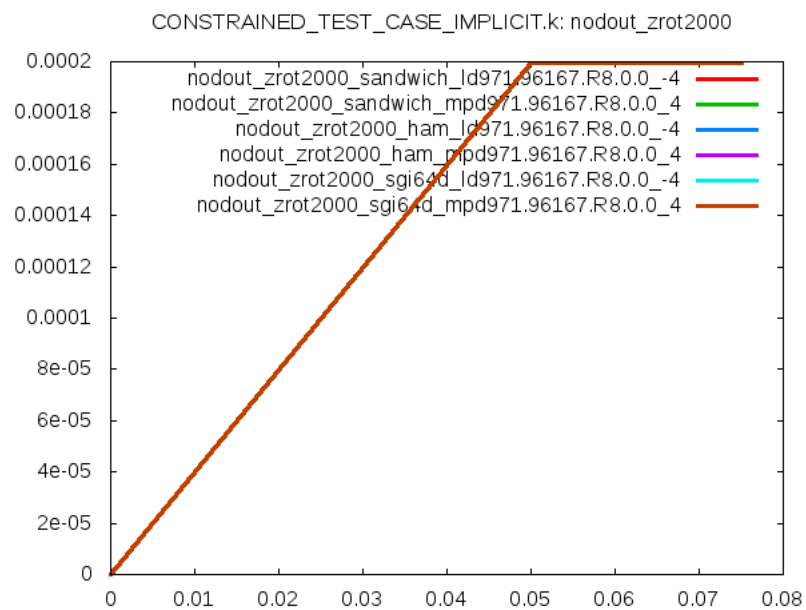


Figure 26: Cross platform results, nodal rotation around z-axis, node 2000, sub test case ID 2

References

- [1] LSTC, *LS-DYNA KEYWORD USER MANUAL*, 7374 Las Positas Road, Livermore, CA, 94551, USA, version 971 ed., May 2007.